


Ordering Information

Basic Configuration Units


CPU Units

■ CJ2H (Built-in EtherNet/IP) CPU Units

Product name	Specifications				Current consumption (A)		Model	Standards
	I/O capacity/ Mountable Units (Expansion Racks)	Program capacity	Data memory capacity	LD instruction execution time	5 V	24 V		
CJ2H (Built-in EtherNet/IP) CPU Units 	2,560 points/ 40 Units (3 Expansion Racks max.)	400K steps	832K words (DM: 32K words, EM: 32K words × 25 banks)	0.016 μs	0.82 (See note.)	---	CJ2H-CPU68-EIP	UC1, N, L, CE
		250K steps	512K words (DM: 32K words, EM: 32K words × 15 banks)				CJ2H-CPU67-EIP	
		150K steps	352K words (DM: 32K words, EM: 32K words × 10 banks)				CJ2H-CPU66-EIP	
		100K steps	160K words (DM: 32K words, EM: 32K words × 4 bank)				CJ2H-CPU65-EIP	
		50K steps	160K words (DM: 32K words, EM: 32K words × 4 bank)				CJ2H-CPU64-EIP	

Note: Add 0.15 A per Adapter when using NT-AL001 RS-232C/RS-222A Adapters.
 Add 0.04 A per Adapter when using CJ1W-CIF11 RS-422A Adapters.
 Add 0.20A/Unit when using NV3W-M□20L(-V1) Programmable Terminals.

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CJ2H CPU Units 	2,560 points/ 40 Units (3 Expansion Racks max.)	400K steps	832K words (DM: 32K words, EM: 32K words × 25 banks)	0.016 μs	0.42 (See note.)	---	CJ2H-CPU68	UC1, N, L, CE
		250K steps	512K words (DM: 32K words, EM: 32K words × 15 banks)				CJ2H-CPU67	
		150K steps	352K words (DM: 32K words, EM: 32K words × 10 banks)				CJ2H-CPU66	
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