Display

The display on the I/O Interface Unit is identical to that on the I/O Control Unit on the CPU Rack. For details on the data displayed in each mode, refer to 2-3-2 I/O Control Units.

Peripheral Device Connector The I/O Interface Unit CV500-II201 for Expansion I/O Racks provides a connector for connecting a Peripheral Device. One Peripheral Device (CVSS or Programming Console) can be connected per to the CPU or I/O Interface Units for each PC, although additional Peripheral Devices can be connected to Slave Racks if a SYSMAC BUS/2 System is used. Whether mounted to the CPU or an I/O Interface Unit, the operation of the Peripheral Device is the same.

Note When connecting a Peripheral Device to the CPU or an I/O Interface Unit, set the baud rate of the CPU to 50k bps via the CPU DIP switch.

2-3-4 Power Supply Units

The Power Supply Unit is available in three models. The CV500-PS221 and CVM1-PA208 run on 100 to 120 VAC or 200 to 240 VAC, and the CV500-PS211 runs on 24 VDC. Both Power Supply Units can be used with any CPU Rack, Expansion CPU Rack, Expansion I/O Rack, or Remote I/O Slave Rack. The table below summarizes the output capacity of the two models.

Model	Supply voltage	Output power
CVM1-PA208	100 to 120/200 to 240 VAC	8 A at 5 VDC
CV500-PS221		12 A at 5 VDC
CV500-PS211	24 VDC	12 A at 5 VDC

Note The total power consumed by each Rack must be within the values stated in the table above. For example, do not mount Units with a total current consumption greater than 12-A to a Rack supplied by a 12-A Power Supply Unit.

CV500-PS221/CVM1-PA208



connections

Rack Components

CV500-PS211



2 - 3 - 5**Termination Resistance Units**

A Termination Resistance Unit (CV500-TER01) must be attached to all unused I/O connectors of I/O Control and I/O Interface Units in the system. Failure to attach Termination Resistance Units to the unused I/O connectors will result in an error and the incorrect operation of the PC. The following example illustrates the locations where Termination Resistance Units are required. Two Termination Resistance Units are provided with the CV500-IC101/201 I/O Control Unit.



Termination **Resistance Unit**

Appendix B Specifications

Power Supply Units

Item	CVM1-PA208	CV500-PS221	CV500-PS211		
Supply voltage	100 to 120 or 200 to 240 VAC, 50/60 Hz		24 VDC		
Operating voltage range	85 to 132 or 170 to 264 VAC	20.4 to 28.8 VDC			
Power consumption	150 VA max.	200 VA max.	100 W max.		
Inrush current	30 A max.		30 A max.		
Output capacity	8 A, 5 VDC	12 A, 5 VDC	12 A, 5 VDC		
Insulation resistance	20 M Ω min. (at 500 VDC) between AC external terminals and $$ (GR) terminals. (See note 1.)				
Dielectric strength	2,300 VAC 50/60 Hz for 1 min between AC external and (⊕(GR) terminals, leakage current: 10 mA max. 1,000 VAC 50/60 Hz for 1 min between DC external and (⊕(GR) terminals, leakage current: 20 mA max. (See note 1.)				
Noise immunity	1,000 Vp-p, pulse width: 100 ns to 1 µs, rise time: 1 ns (via noise simulation)				
Vibration resistance	10 to 58 Hz, 0.075-mm amplitude, 58 to 150 Hz, acceleration: 9.8 m/s ² (see note 2) in X, Y, and Z directions for 80 minutes (Time coefficient; 8 minutes x coefficient factor $10 =$ total time 80 minutes) (according to JIS C0040)				
Shock resistance	147 m/s ² 3 times each in X, Y, and Z directions (according to JIS C0041)				
Ambient temperature	Operating: 0° to 55°C Storage: –20° to 75°C (except Memory Card and battery)				
Humidity	10% to 90% (with no condensation)				
Atmosphere	Must be free from corrosive gasses				
Grounding	Less than 100 Ω				
Enclosure	Mounted in a panel				
Weight	All models are each 9 kilograms max.				
Dimensions (without cables, see note 3)	CPU (10 slots)/Expansion CPU/Expansion I/O (11 slots): 480 x 250 x 123 mm (WxHxD) CPU (5 slots)/Expansion I/O (6 slots): 306 x 250 x 123 mm (WxHxD) CPU (3 slots)/Expansion I/O (4 slots): 236 x 250 x 123 mm (WxHxD)				

Note 1. Disconnect the ♠ (LG) terminal of the Power Supply Unit from the ⊕ (GR) terminal when performing insulation and dielectric strength tests. If the tests are repeatedly performed with the ♠ (LG) and ⊕ (GR) terminals short-circuited, the internal components may be damaged.

2. Acceleration (G)

